

## SB7560S 75A SCR

### FEATURES

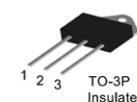
- High thermal cycling performance
- High voltage capacity
- Very high current surge capability

### APPLICATIONS

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- AC power control
- Lighting and temperature control

### Parameters Summary

V<sub>DSS</sub>=V<sub>DRM</sub>=100V, I<sub>SM</sub>=75A, V<sub>G</sub>=12V, I<sub>G</sub>=50mA



### ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T <sub>stg</sub>	-40 ~ 150	°C
Operating junction temperature range	T <sub>j</sub>	-40 ~ 125	°C
Repetitive peak off-state voltage (T = 25°C)	V <sub>DRM</sub>	1200/1600	V
Repetitive peak reverse voltage (T = 25°C)	V <sub>RRM</sub>	1200/1600	V
Non repetitive surge peak Off-state voltage	V <sub>DSSM</sub>	V <sub>DSS</sub> +100	V
Non repetitive peak reverse voltage	V <sub>RRM</sub>	V <sub>RRM</sub> +100	V
RMS on-state current (T = 100°C)	I <sub>T(RMS)</sub>	75	A
Non repetitive surge peak on-state current	I <sub>TSM</sub>	700	A
I <sup>2</sup> t value for fusing (tp=10ms)	I <sup>2</sup> t	2450	A <sup>2</sup> s
Critical rate of rise of on-state current (I = 2×IGT, tr ≤ 100 ns)	di/dt	150	A/μS
Peak gate current	I <sub>GM</sub>	5	A
Average gate power dissipation	P <sub>G(AV)</sub>	2	W

### Thermal Resistances

Symbol	Parameter	Value	Unit
R <sub>th(j-c)</sub>	Junction to case (DC)	TO-3P	0.60
		TO-247	0.55
			°C/W

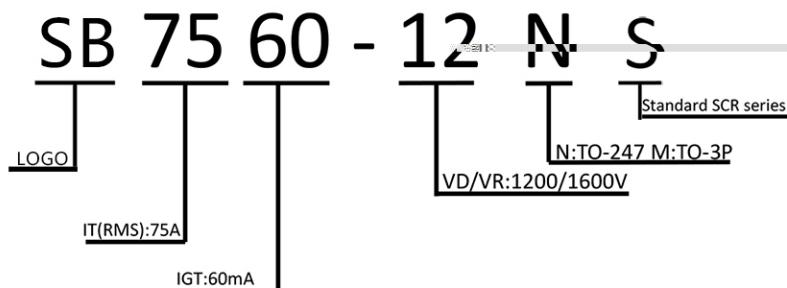
### ELECTRICAL CHARACTERISTICS ( $T = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Test Condition	Parameter	Value (Max.)
$I_{GT}$	$V_{GT} = 12\text{V}$ , $R = 1\text{k}\Omega$	$I_{LMAX}$	100
$V_{GT}$		$t_{Rising}$	20
$V_{CP}$	$V_D = V_{VRM}$ , $I_L = I_{LMAX}$	$t_{Falling}$	10
$I_L$		$t_{ON}$	200
$I_T$	$V_D = V_{VRM}$ , $I_L = I_{LMAX}$	$t_{OFF}$	100
$dV/dt$	$V_D = V_{VRM}$ , $I_L = I_{LMAX}$	$t_{Recovery}$	100

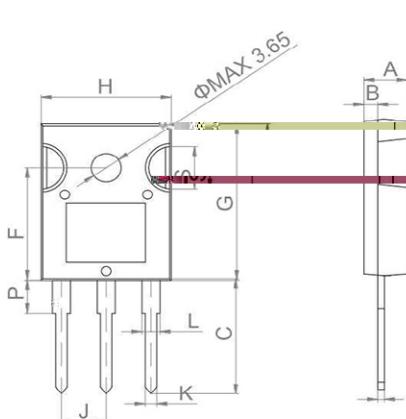
### STATIC CHARACTERISTICS

Symbol	Parameter	Value (MAX.)
$V_{TM}$	$ITM = 140\text{A}$ $t_p = 380\mu\text{s}$	140
$I_{DRM}$	$V_D = V_{VRM}$ , $I_L = I_{LMAX}$	200,000
$I_{RRM}$	$V_D = V_{VRM}$ , $I_L = I_{LMAX}$	200,000

### Ordering Information Scheme

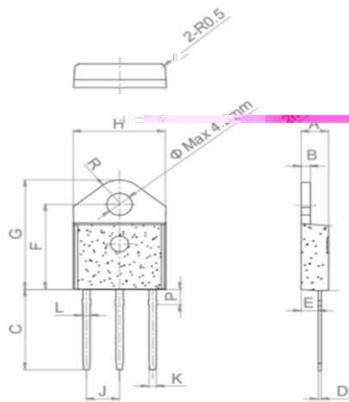


### TO-247 Package Mechanical Data



Dimensions		Dimensions	Dimensions
Min.	Max.	Min.	Max.
1.16	1.27	20.2	21.0
14.35	15.54	10.85	11.05
0.57	0.87	0.020	0.025
14.4	15.1	1.7	1.8
12.1	10.7	20.3	20.6
15.4	16.2	0.606	0.648
5.3	5.6	0.209	0.220
1.5	1.5	0.051	0.050
5.35	5.63	0.211	0.214

## TO-3P Package Mechanical Data



Ref.	Dimensions					
	Millimeters			Inches		
	Min	Typ	Max	Min	Typ	Max
A	4.40		4.60	0.173		0.181
B	1.40		1.60	0.055		0.062
C	15.48		15.88	0.608		0.635
D	0.50		0.70	0.019		0.027
E	2.70		2.90	0.106		0.114
F	15.92		16.32	0.626		0.642
G	20.27		20.67	0.798		0.803
H	15.15		15.35	0.590		0.604
J		5.45			0.214	0.216
K	1.10		1.30	0.043		0.051
L	1.15		1.35	0.045		0.053
P	2.68		3.08	0.105		0.121
R		4.20			0.165	

FIG.1 Maximum power dissipation versus on-state current

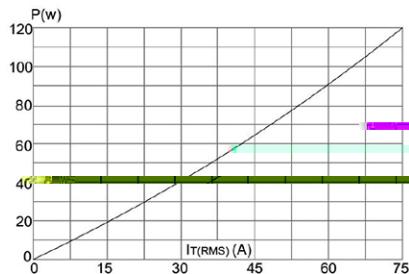


FIG.2: on-state current versus case temperature

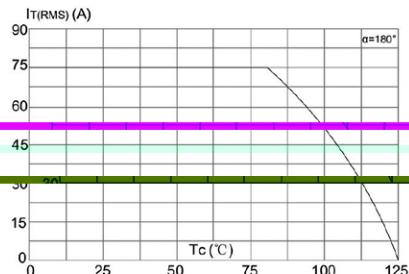


FIG.3: Surge peak on-state current versus number of cycles

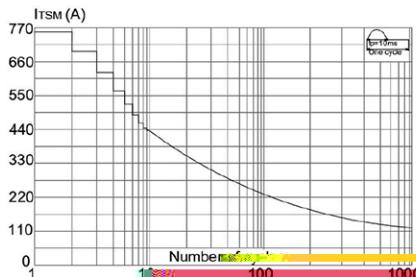


FIG.4: On-state characteristics (maximum values)

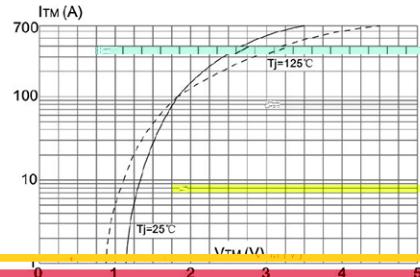


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms, and corresponding value of I2 t (dI/dt < 50A/μs)

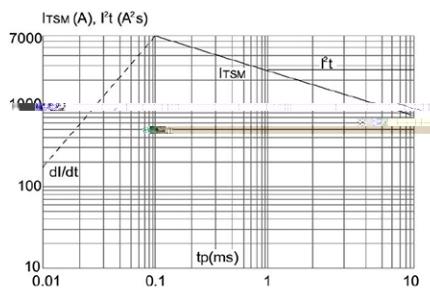


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

